# FFF TRANSMITTAL

# Electronic Version v08

Stylesheet Version v08.0

Title of Invention

Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages

Application Number:

Date:

First Named Applicant: Mr. Bhupendra SHARMA

Attorney Docket Number: TI-38242

# TOTAL FEE AUTHORIZED \$ 770

Patent fees are subject to annual revisions on or about October 1st of each year.

Filing as large entity

#### BASIC FILING FEE

Fee Description	Fee Code	Amount \$	Fee Paid \$			
Utility Filing Fee	1001	770	770			
Subtotal For Basic Filing Fees: \$ 770						

## EXTRA CLAIM FEES

Fee Description	Extra Claim	Fee Code	Amount \$	Fee Paid \$	
Total Claims: 11	0	1202	18	0	
Independent Claims: 03	0	1201	86	0	
Subtotal For Extra Claims Fees: \$ 0					

## AUTHORIZED BILLING INFORMATION

The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit account number: 200668

Deposit name: Texas Instruments Incorporated

Deposit authorized name: Narendra R. Thappeta Signature: /Narendra Reddy Thappeta/

Date (YYYYMMDD): 2004-09-24

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.